

Re: Ever heard of Potato Semiconductor?

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*Source:* <http://sci.tech-archive.net/Archive/sci.electronics.design/2008-07/msg00841.html>

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- *From:* [bill.sloman@xxxxxxxx](mailto:bill.sloman@xxxxxxxx)
  - *Date:* Sun, 6 Jul 2008 19:34:54 -0700 (PDT)
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On Jul 7, 5:39 am, Jim Thompson <To-Email-Use-The-Envelope-I...@My-Web-Site.com> wrote:

On Sun, 06 Jul 2008 12:27:26 -0700, Tim Wescott <t...@xxxxxxxxxxxxxxxxxxxx> wrote:

Jim Thompson wrote:

On Sun, 06 Jul 2008 11:42:09 -0700, John Larkin  
<jjlar...@xx> wrote:

On Sun, 06 Jul 2008 11:28:23 -0700, Jim  
Thompson  
<To-Email-Use-The-Envelope-I...@xxxxxxxxxxxxxxxxxxxx>  
wrote:

-- snip --

Pico-second speeds in CMOS are power hogs... my usual  
design  
cross-over point is around 250-300MHz.

In a WiFi repeater chip I come from 5GHz down to 250MHz  
in CML, then  
CMOS.

Re: Ever heard of Potato Semiconductor?

...Jim Thompson

Yes, but you went to school back when "circuit design" meant "circuit design", not "simulating things you don't understand on software you don't understand using a process you don't understand."

Yep. I was educated 1958–1962 BC (before computers ;-)

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Must have been a crap university – the University of Melbourne in Australia had a computer on-site from 1956

<http://en.wikipedia.org/wiki/CSIRAC>

I used to take my punched cards over to CSIRAC in the Physics Department in 1963. CSIRAC wasn't running then, and the punched cards were taken from there to the university's IBM 7040/44 which was located at the IBM office downtown at that time. The following year the 7040/44 got shifted up to the university, and I eventually qualified to operate it – I did most of my number crunching for my Ph.D. project hands-on between 2.00am and 6.00am on that machine. The electronic engineers kept the 11.00pm to 2.00am slot more or less permanently booked.

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Bill Sloman, Nijmegen

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